



D0 Status Report

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FNAL

All Experimenters' Meeting
10 January 2011



Significant Events

- Late Thursday afternoon, the voltage on one of the power supplies for our interface boards jumped from 5.5 V to 6.1 V.
 - This is outside the normal operating range and could potentially damage the silicon.
 - 1/6th of the silicon was out of the readout for store 8395.
- D0 had a 10 hour access on Friday to recover the supply.
 - Several calorimeter preamp power supplies were also repaired/replaced.
 - 3% of the A-layer PDT system was recovered.
 - Minor work on L1 Muon, silicon, and fiber tracker was performed.



Data Taking

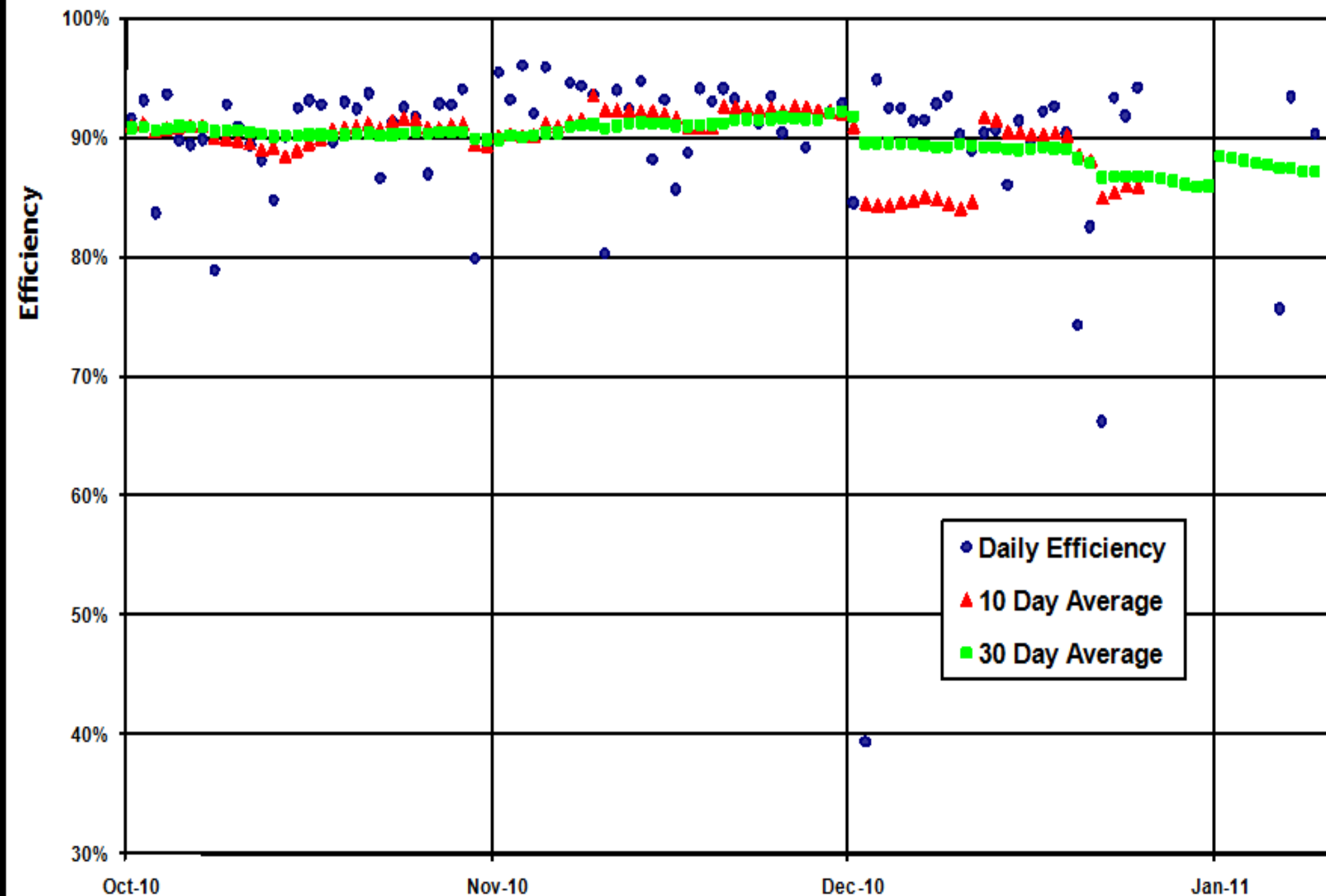
	Delivered Lum (pb ⁻¹)	Recorded Lum (pb ⁻¹)	Efficiency (%)	Comment
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3 Jan	0.00	0.00		
4 Jan	0.00	0.00		
5 Jan	0.00	0.00		
6 Jan	0.68	0.51 (0)	76 (0)	Silicon power supply issue.
7 Jan	3.71	3.47 (0)	94 (0)	Long access to recover the Silicon power supply
8 Jan	0.00	0.00		Opportunistic access to replace a failing blower.
9 Jan	6.41	5.80	90	
3-9 Jan	10.8	9.8 (5.8)	91 (54)	(Data with full detector)



Daily Data Taking Efficiency

1 October 2010 - 9 January 2011





Run II Integrated Luminosity

19 April 2002 - 9 January 2011

